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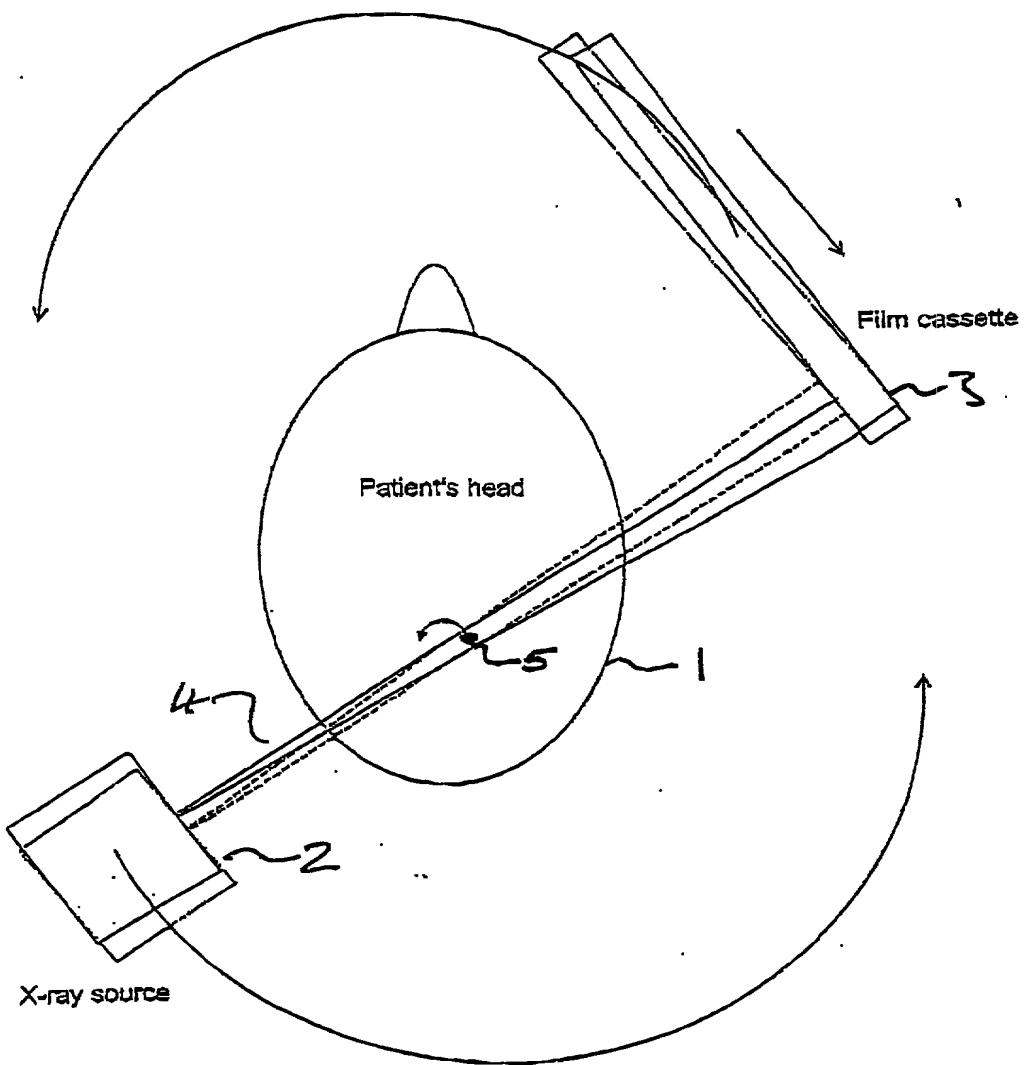


Fig. 1. Panoramic X-ray imaging

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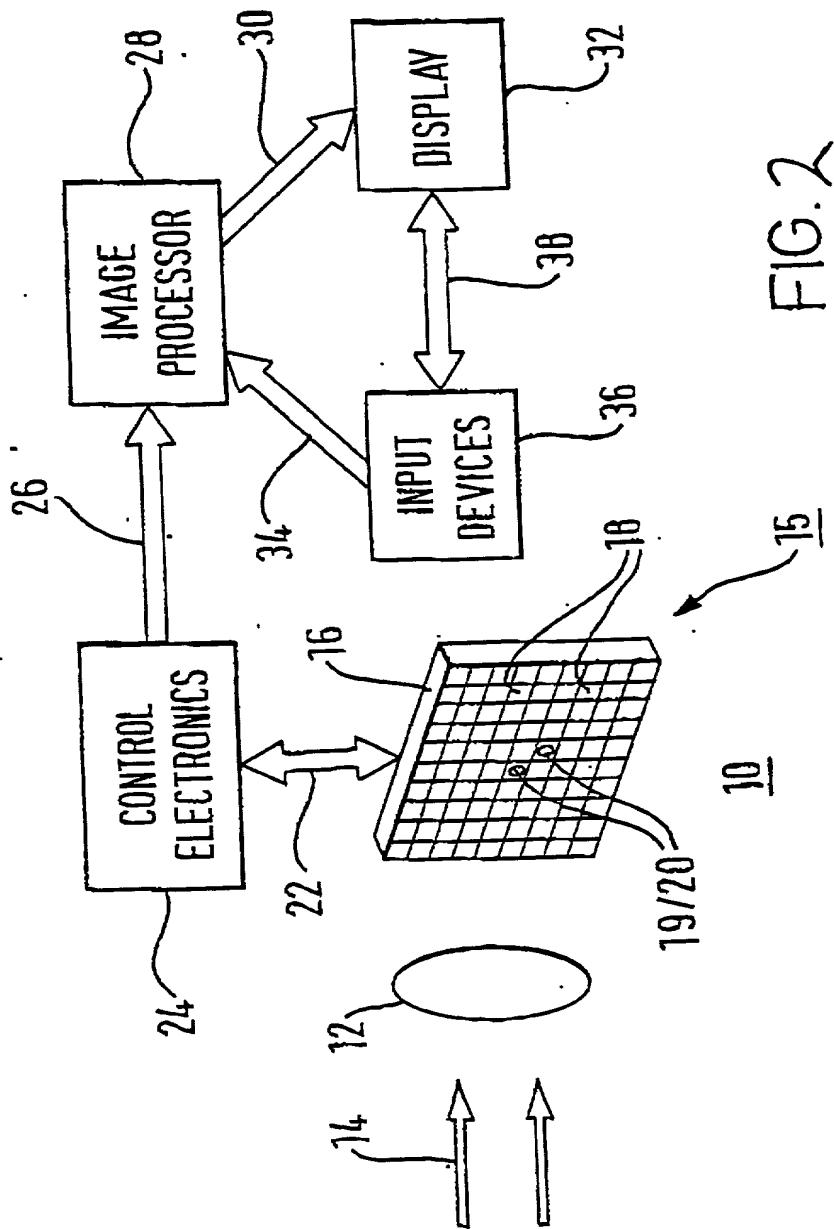


FIG. 2

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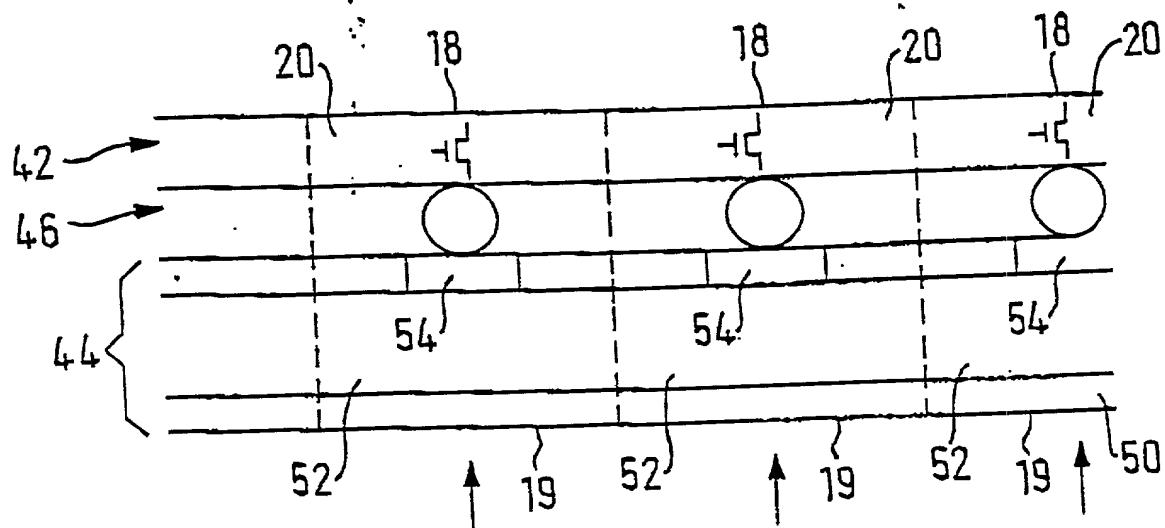


FIG. 3

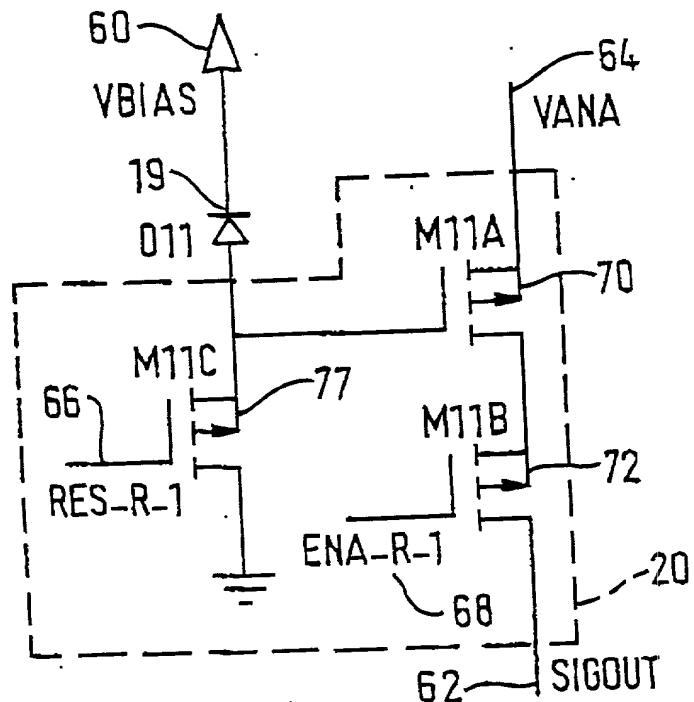
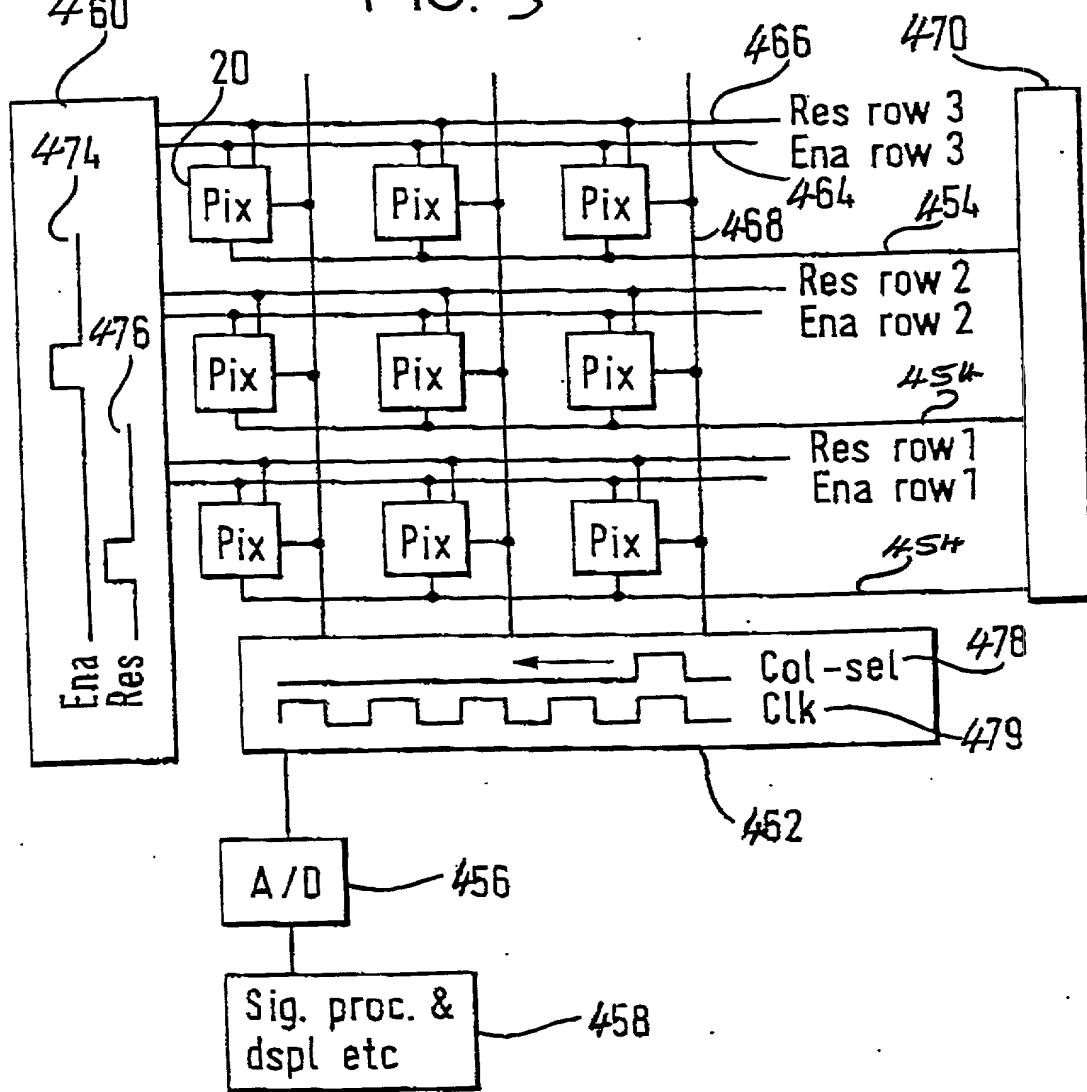


FIG. 4

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FIG. 5



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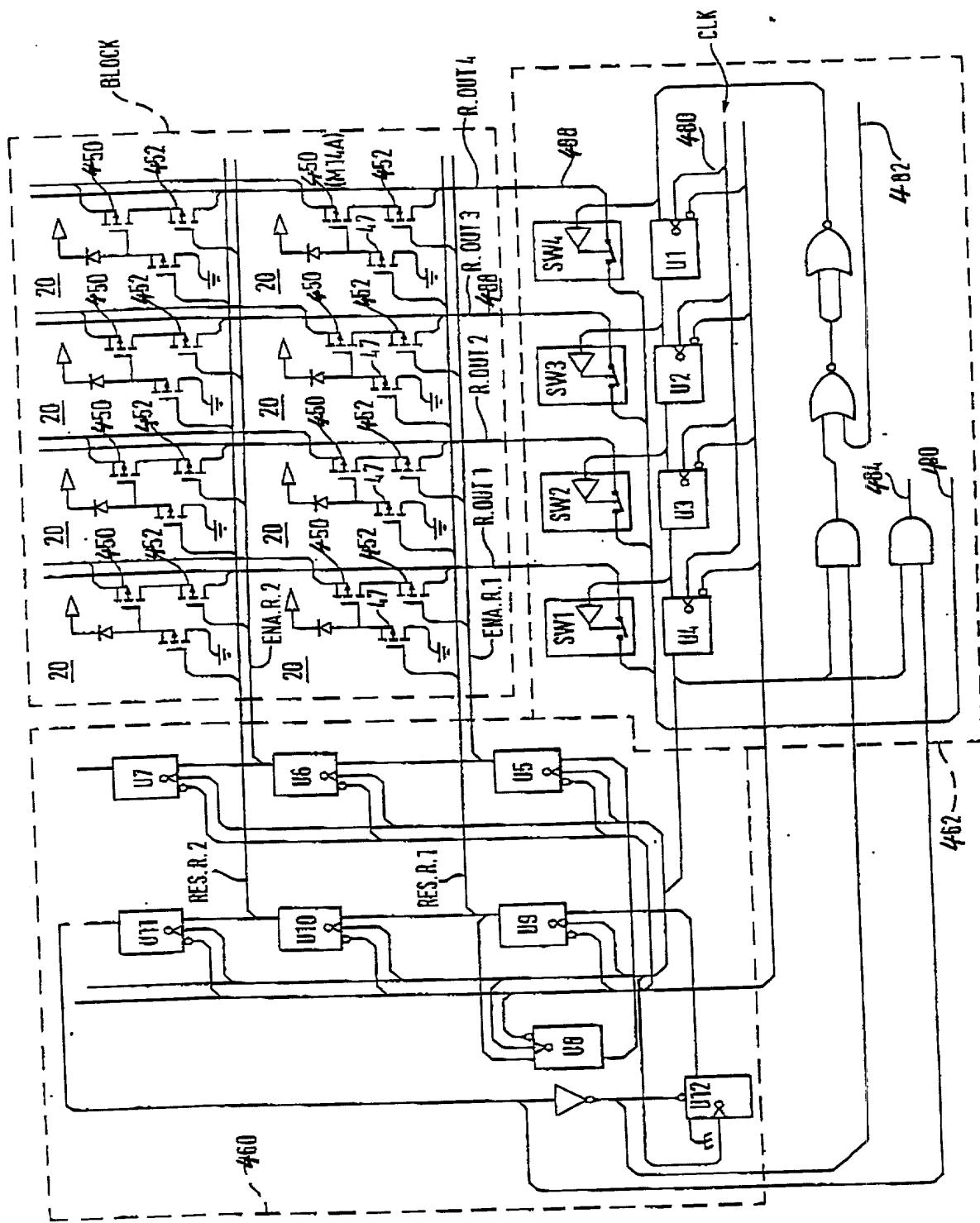
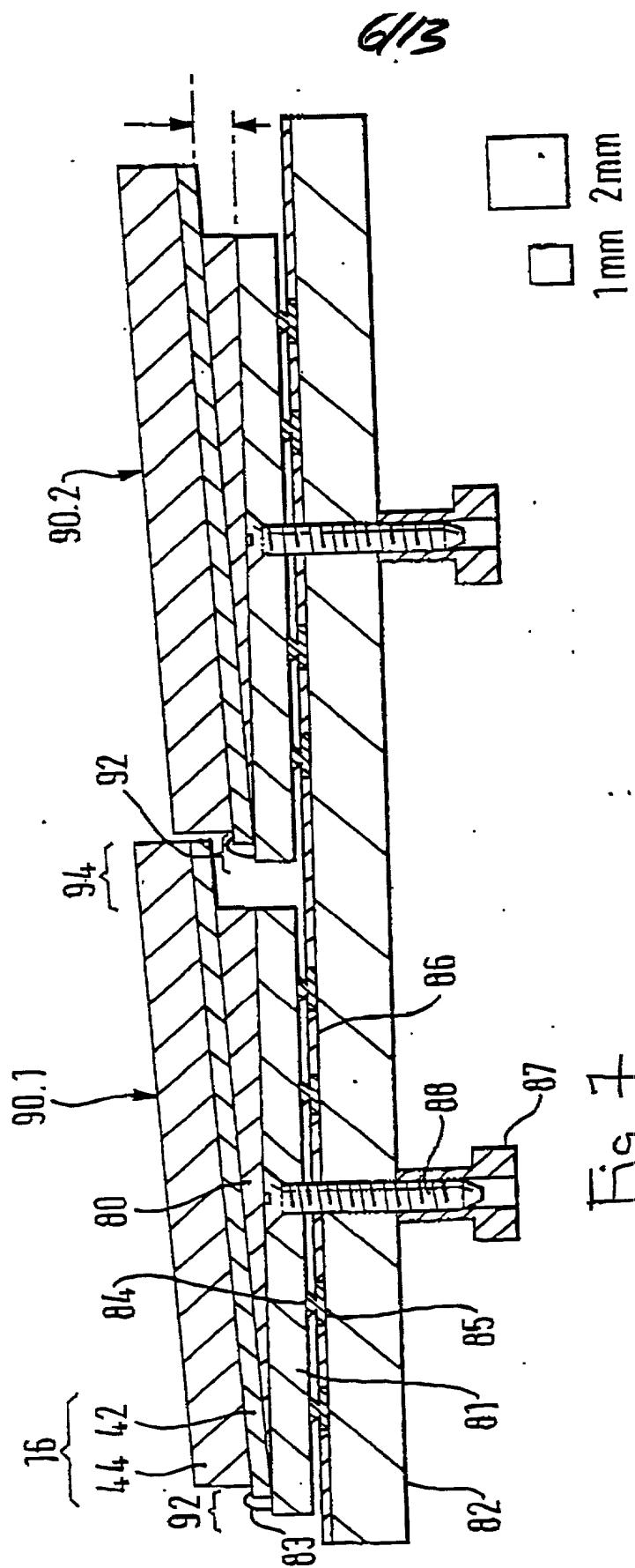


Fig. 6



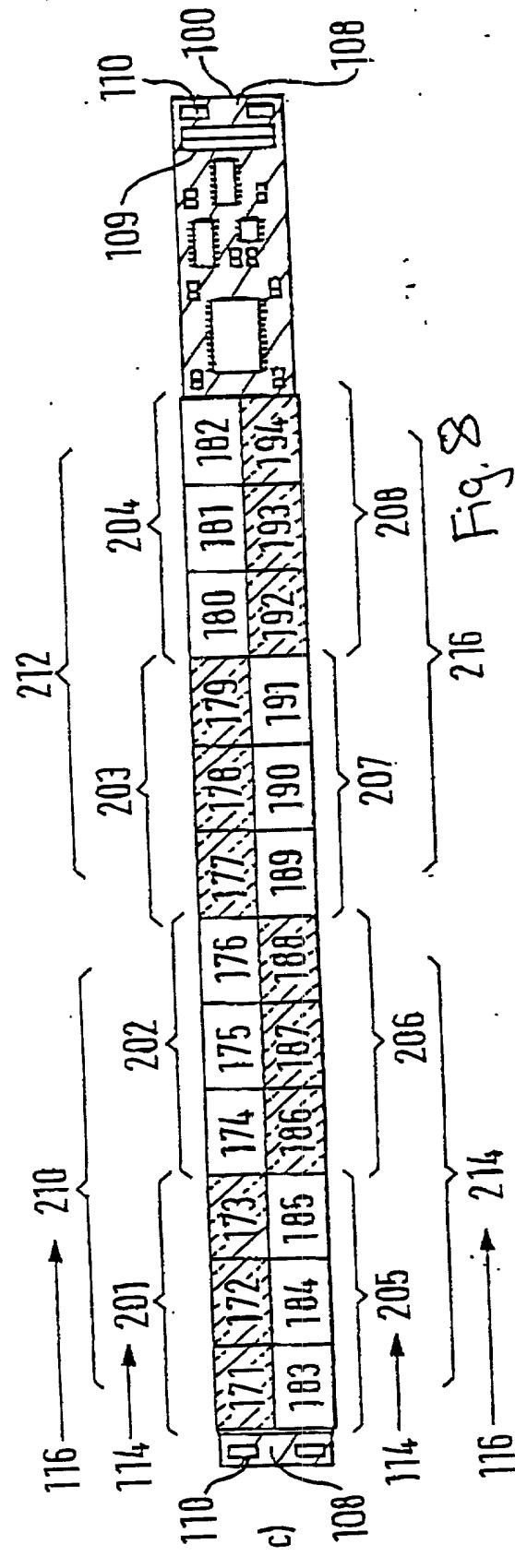
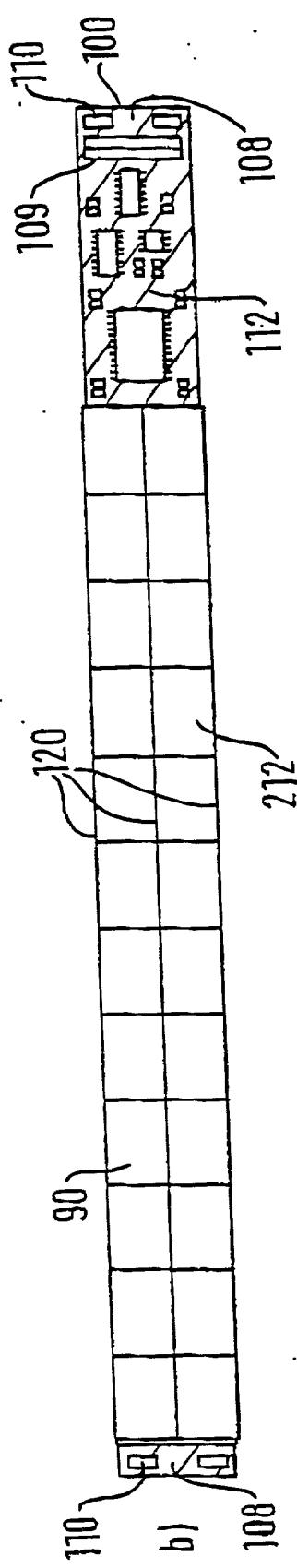
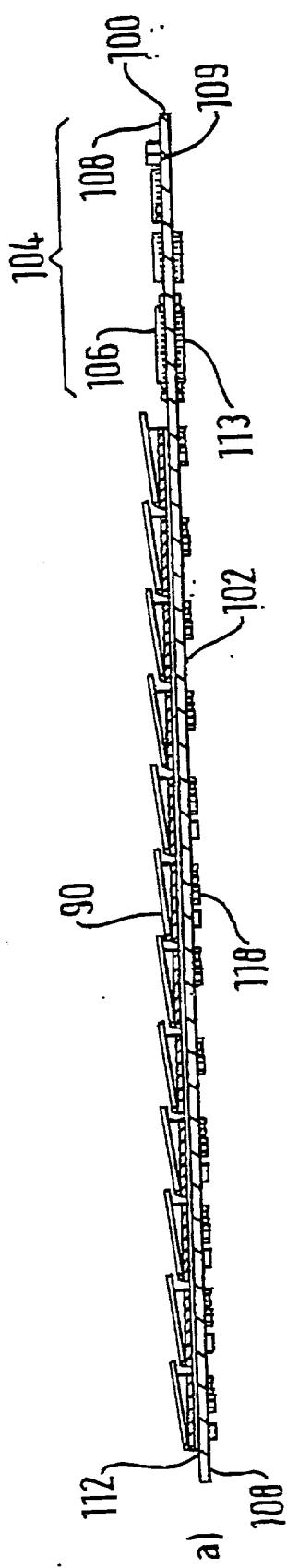


Fig. 8

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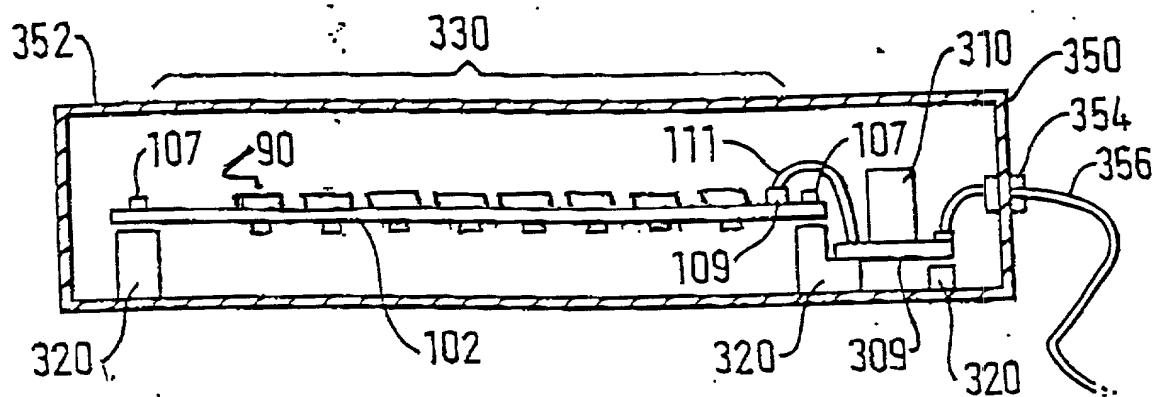


FIG. 9

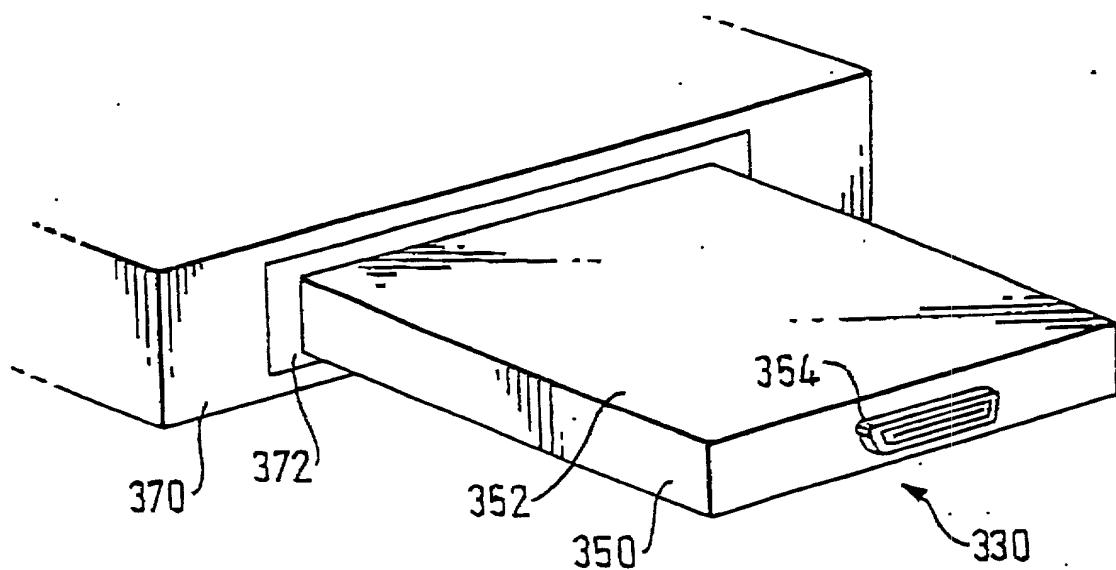


FIG. 10

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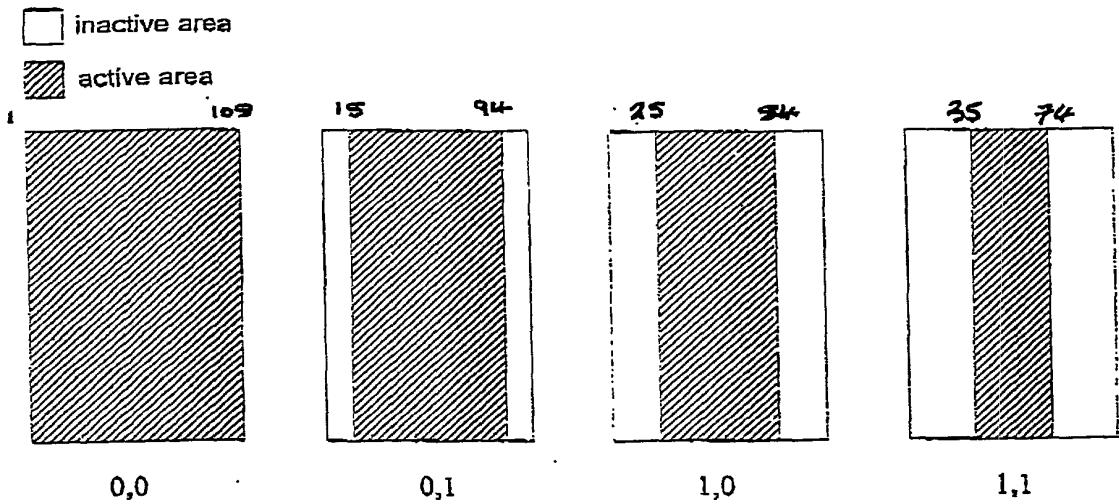


Fig. 11

Table 1 Operating modes

mode	mode0	start row	end row	description
0,0	1	108		whole chip
0,1	15	94		8 mm window
1,0	25	84		6 mm window
1,1	35	74		4 mm window

Fig 12

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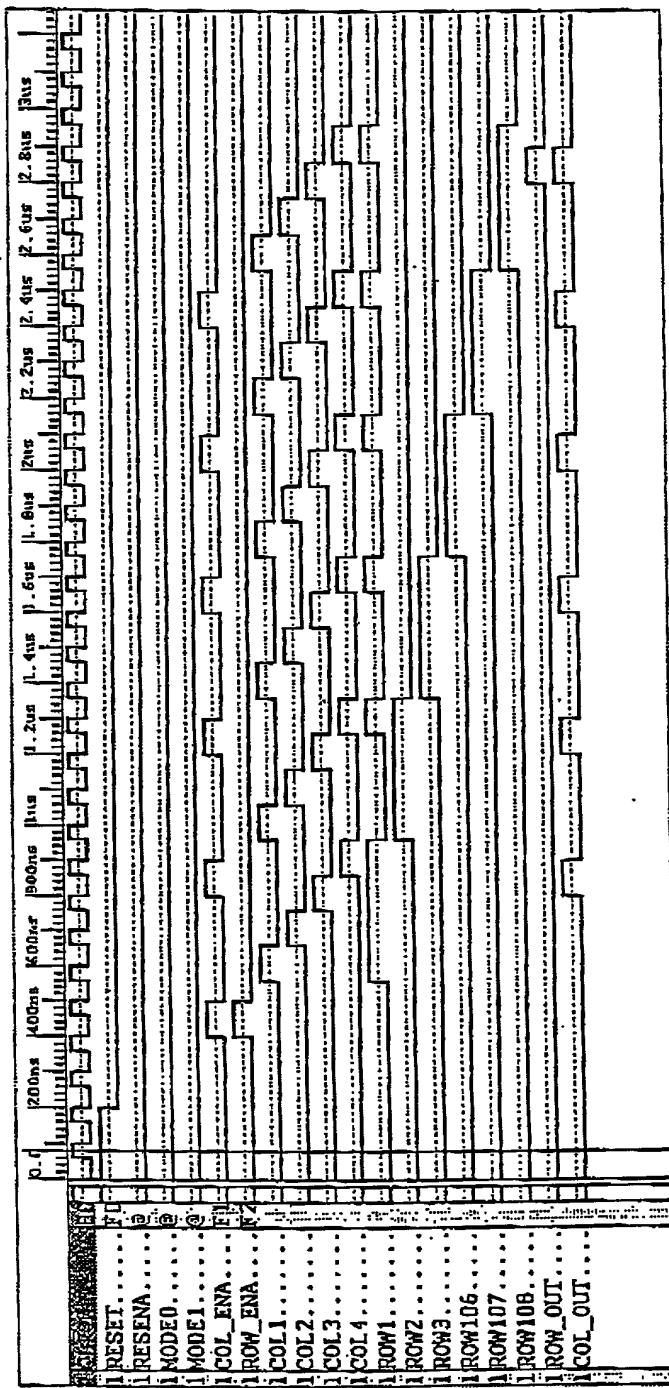


Fig. 13 Timing diagram for one tile in mode (0,0)

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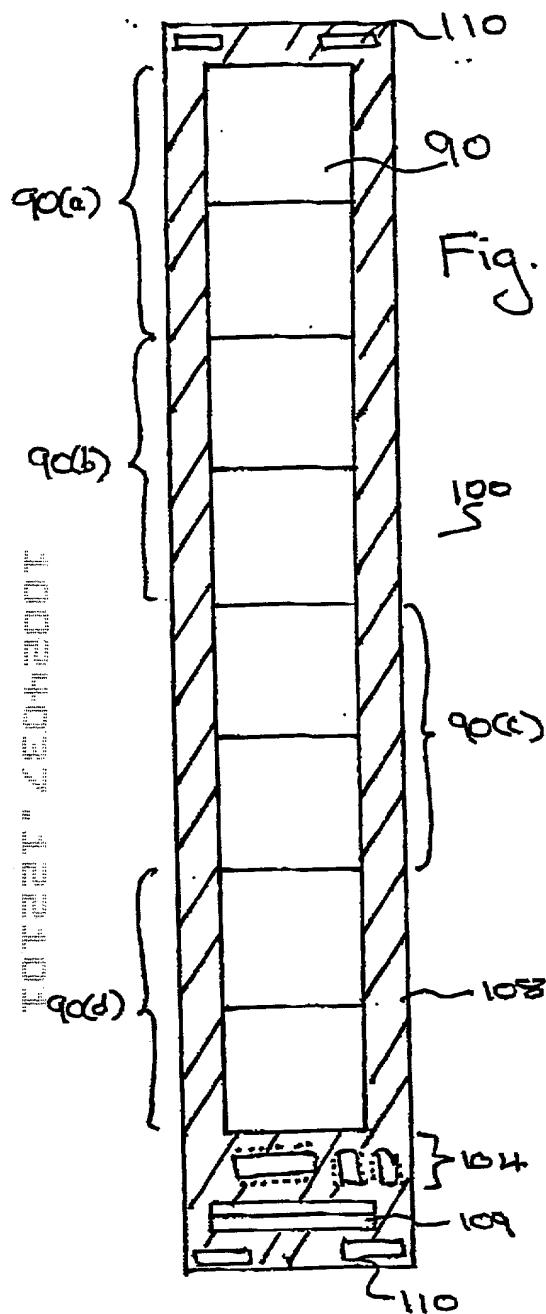
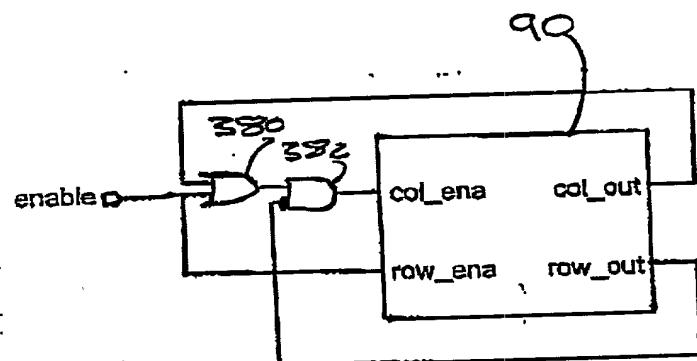


Fig. 14



Example schematic for using one tile

Fig. 15

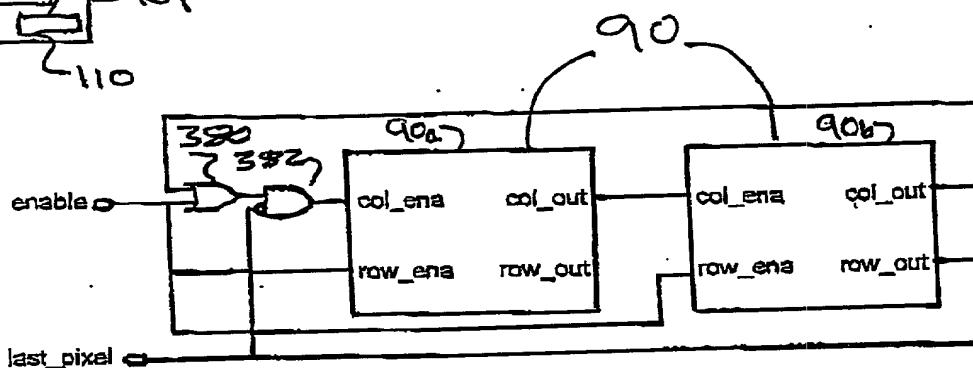
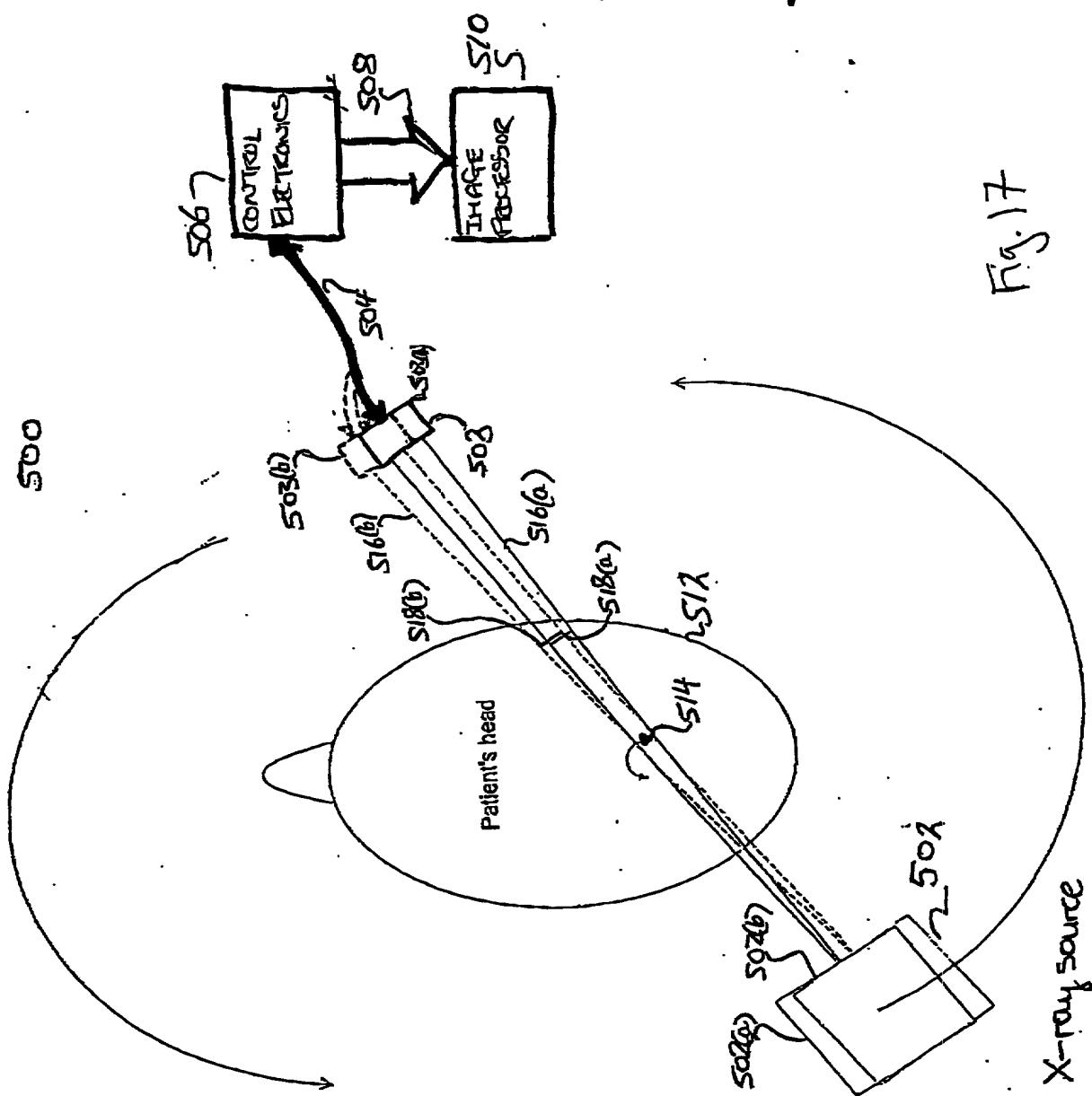


Fig. 16 Example schematic for connecting two tiles as one

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Fig. 17



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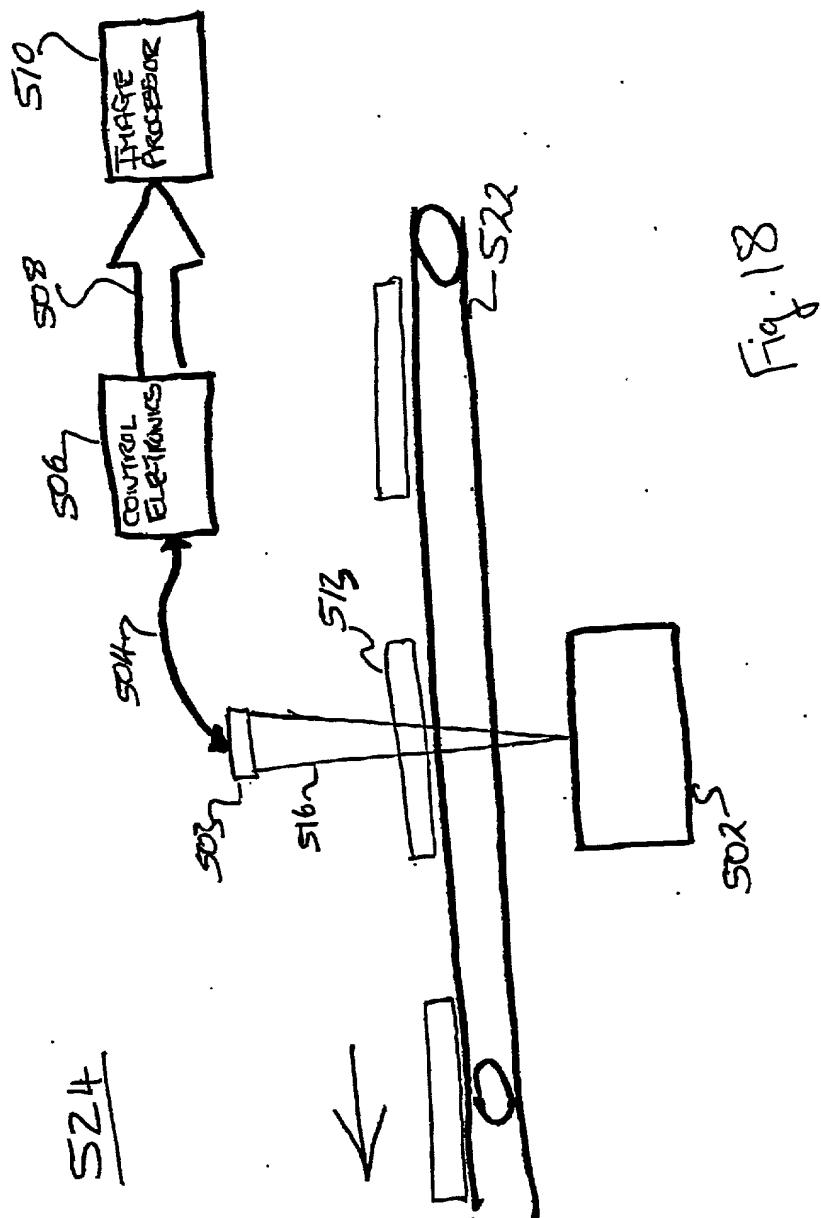


Fig. 18